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NOTES:

1) A "looped-timed" application is shown.
2) This application assumes the dropped channels occupy the same timeslots as the inserted channels.
3) The idle registers in the DS2152, DS2154, DS21X5Y, and DS2155 can be used to fill unused (if any) channels.
4) The used channels do not need to be contiguous nor do they need to be only one channel wide.
5) The DS1000-100 delay line is used to adjust the clock to account for the delay in signal generations that create the bursty clock.
6) Additional channels can be added by allowing the RCHCLK signal to be driven into a counter and decoded; the decoded signal would then be used to provide additional selects for the decoder and mux.

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